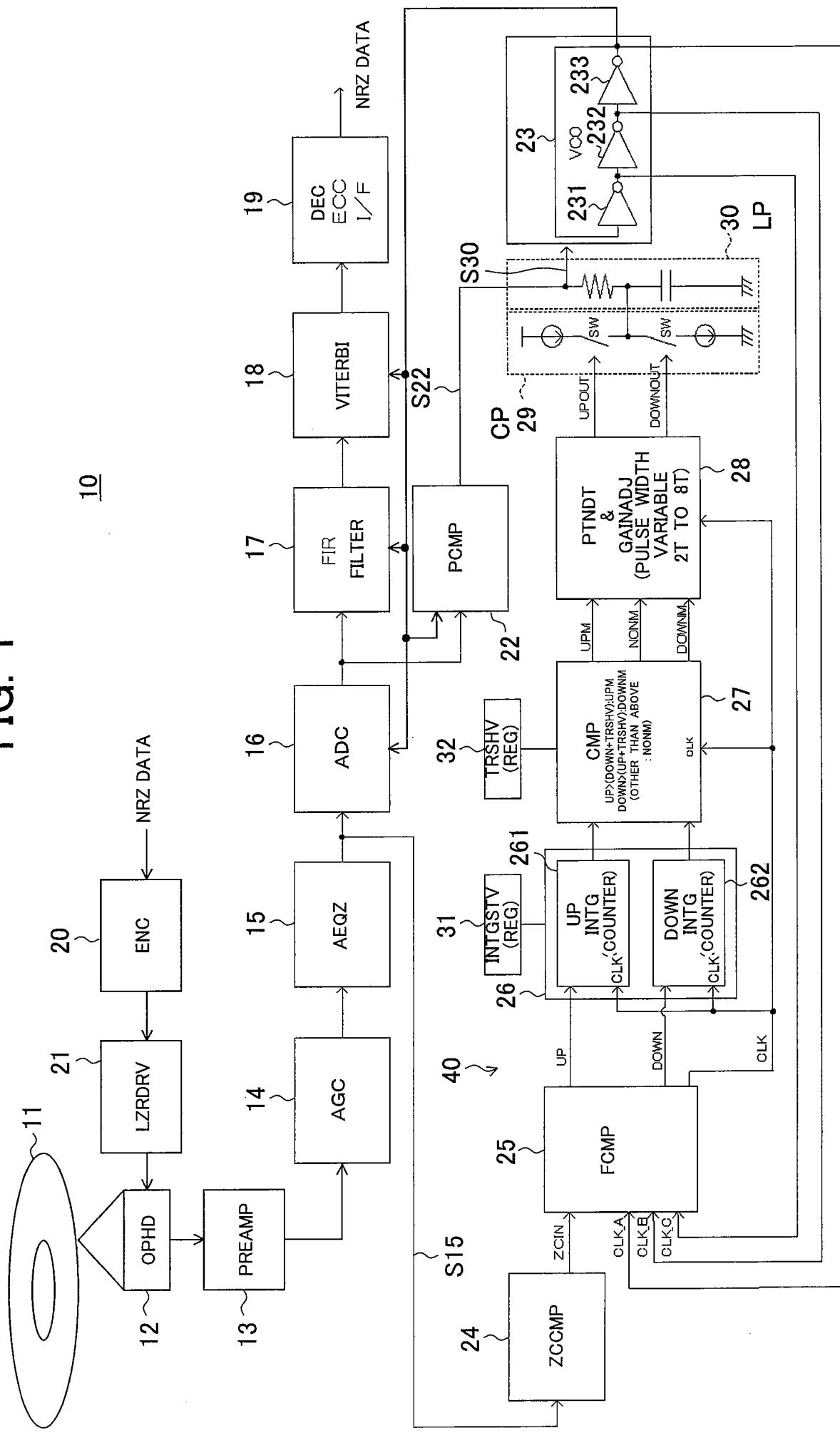


FIG. 1



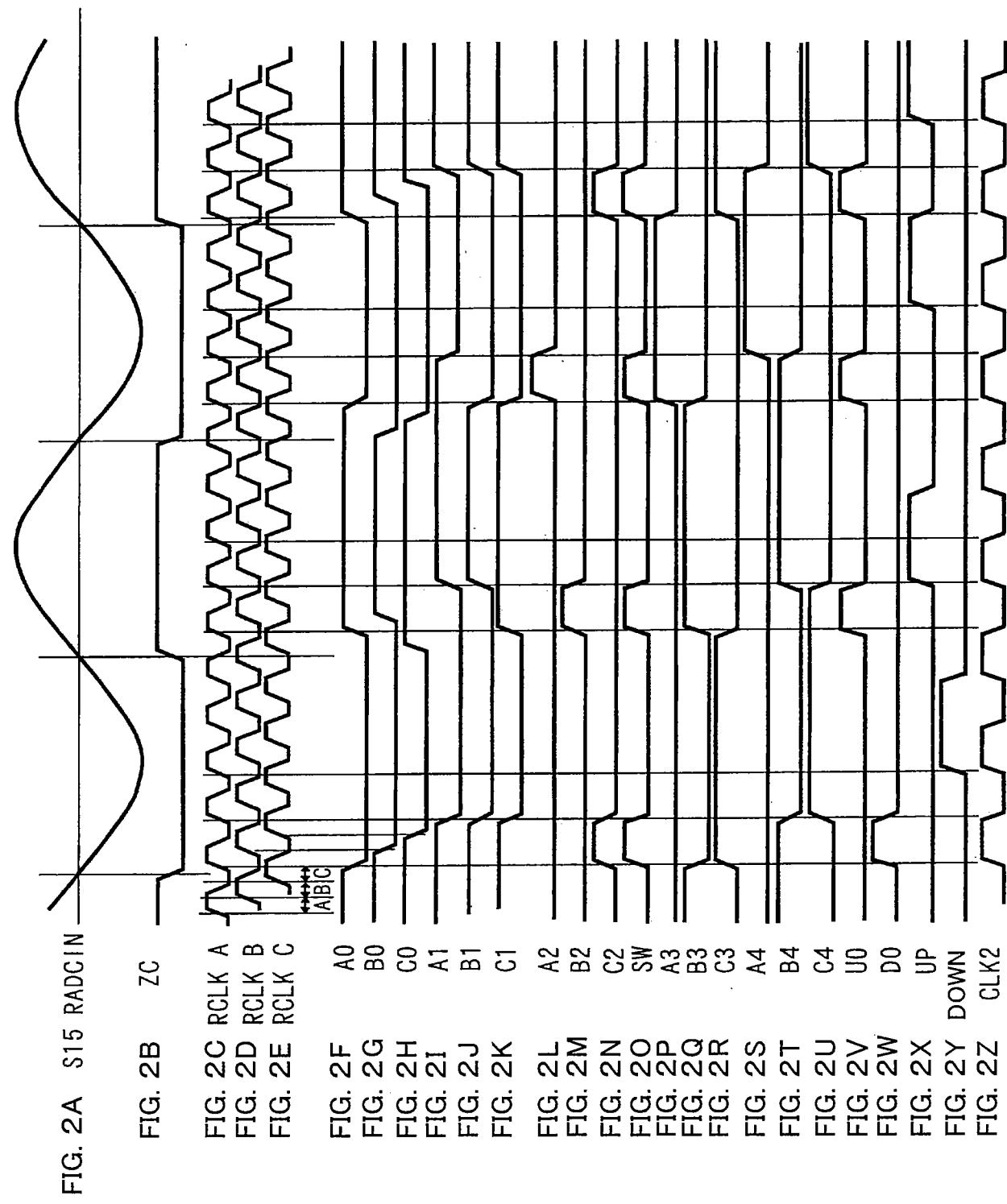


FIG. 3

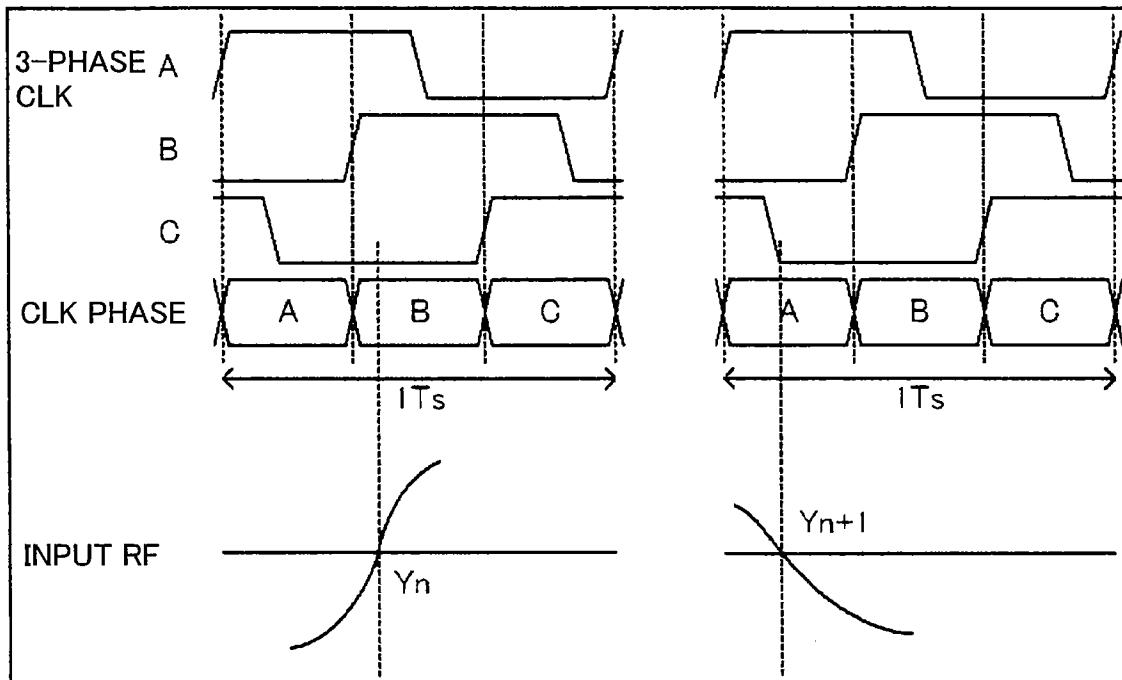


FIG. 4

## FREQUENCY COMPARATOR LOGIC

$Y_n$	$Y_{n+1}$	UP	DOWN
A	A	0	0
A	B	0	1
A	C	1	0
B	A	1	0
B	B	0	0
B	C	0	1
C	A	0	1
C	B	1	0
C	C	0	0

FIG. 5

25

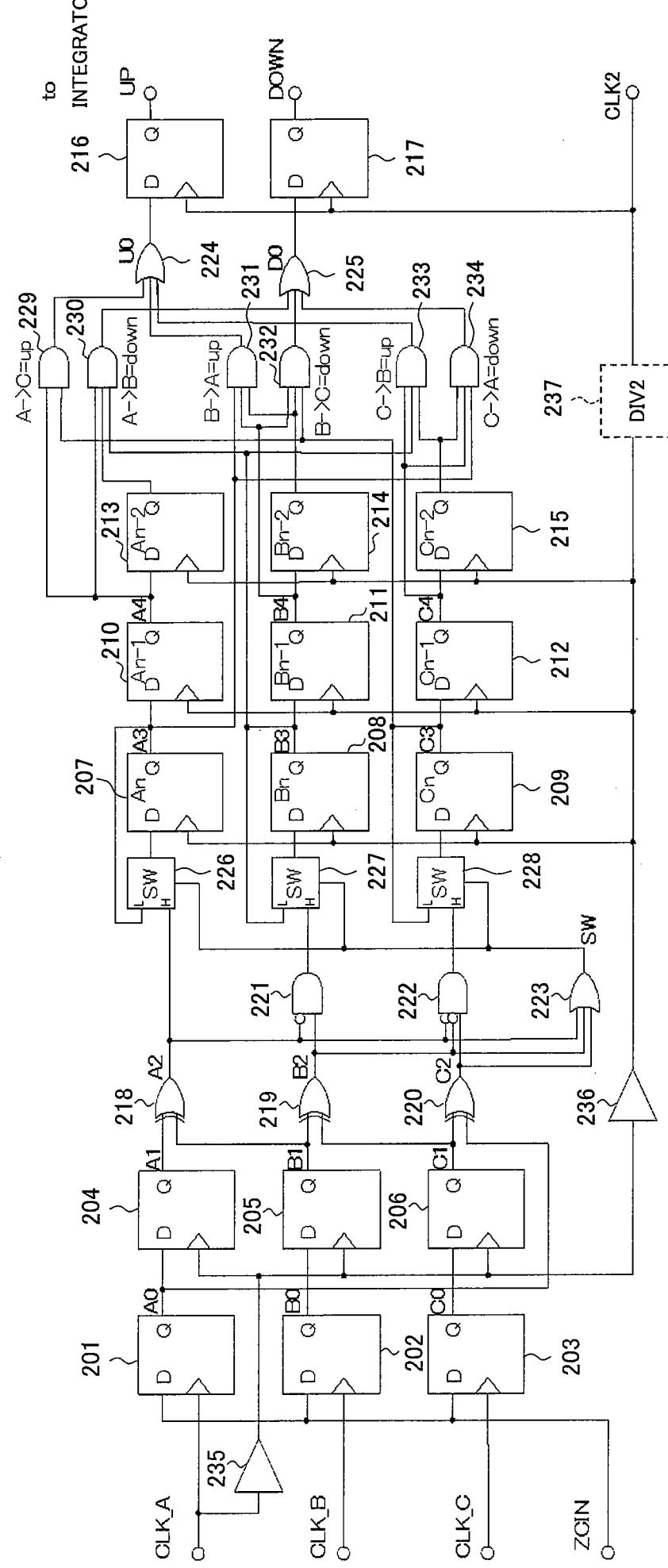
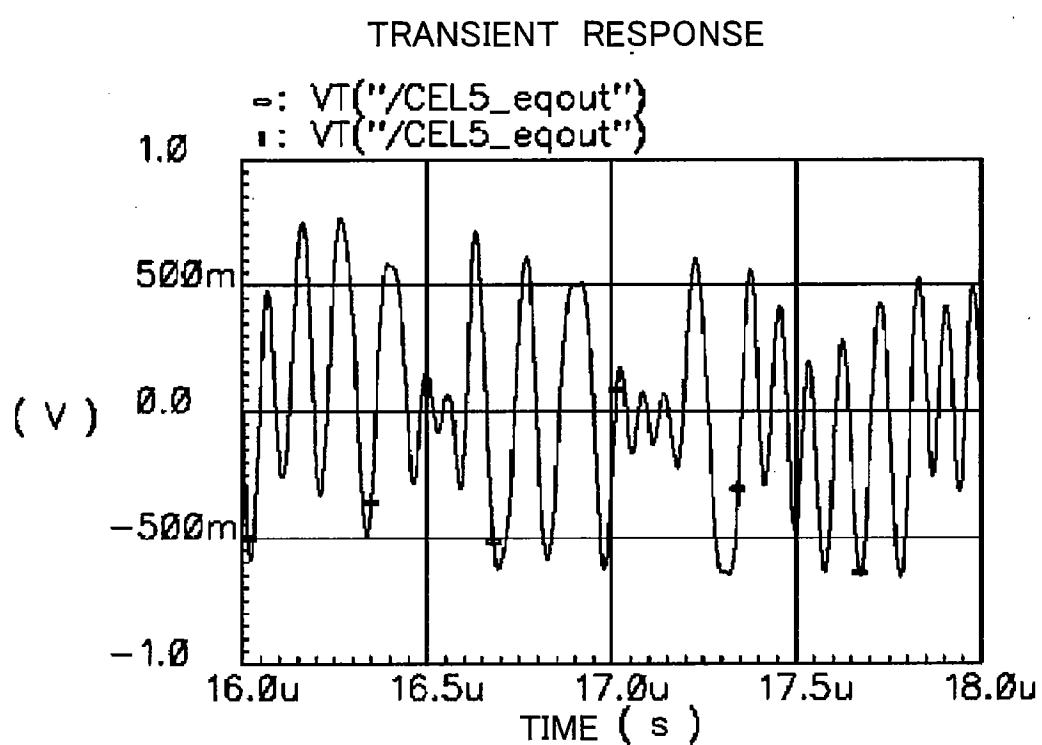


FIG. 6



EXAMPLE OF ZERO CROSS DISTRIBUTION

FIG. 7

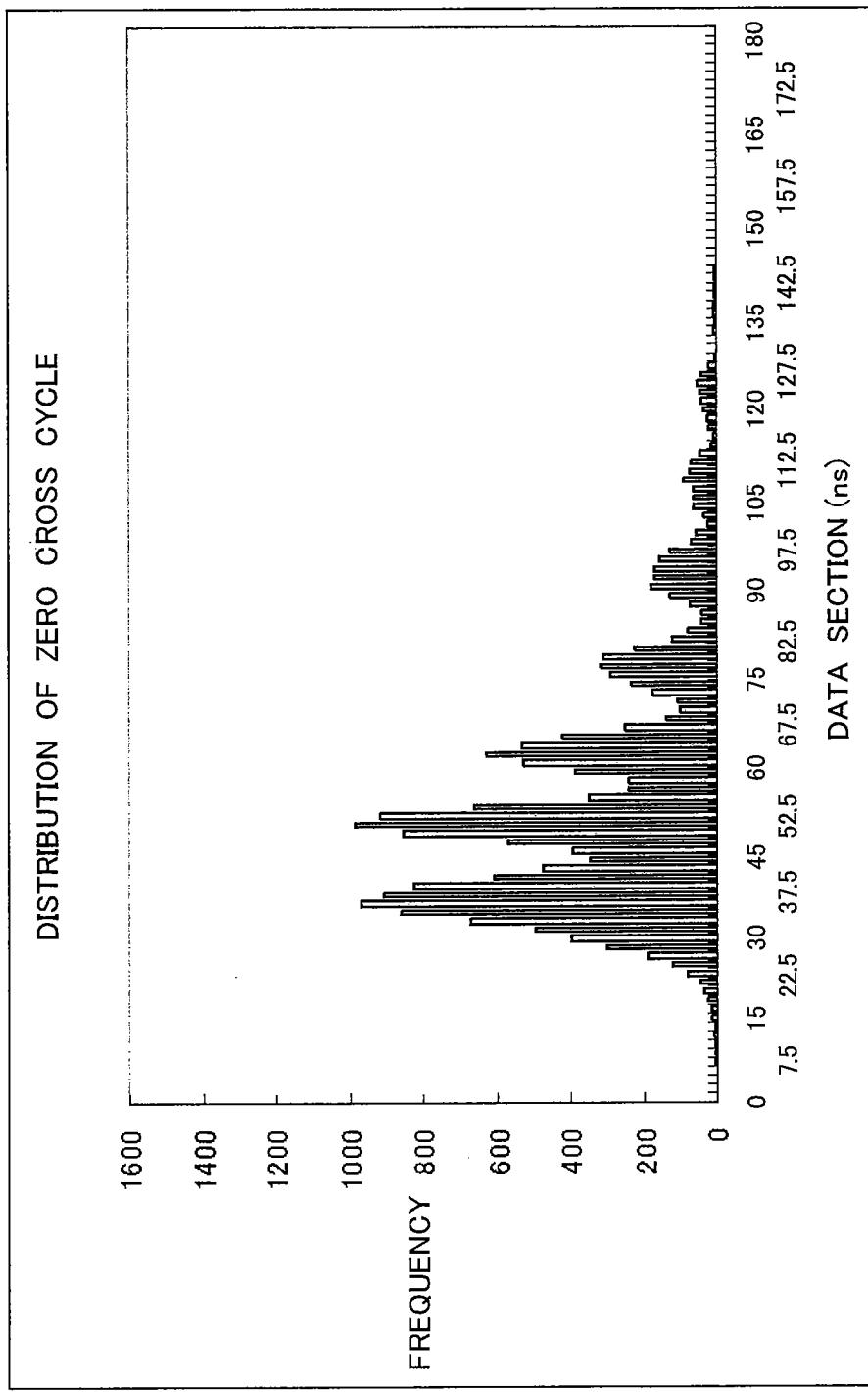
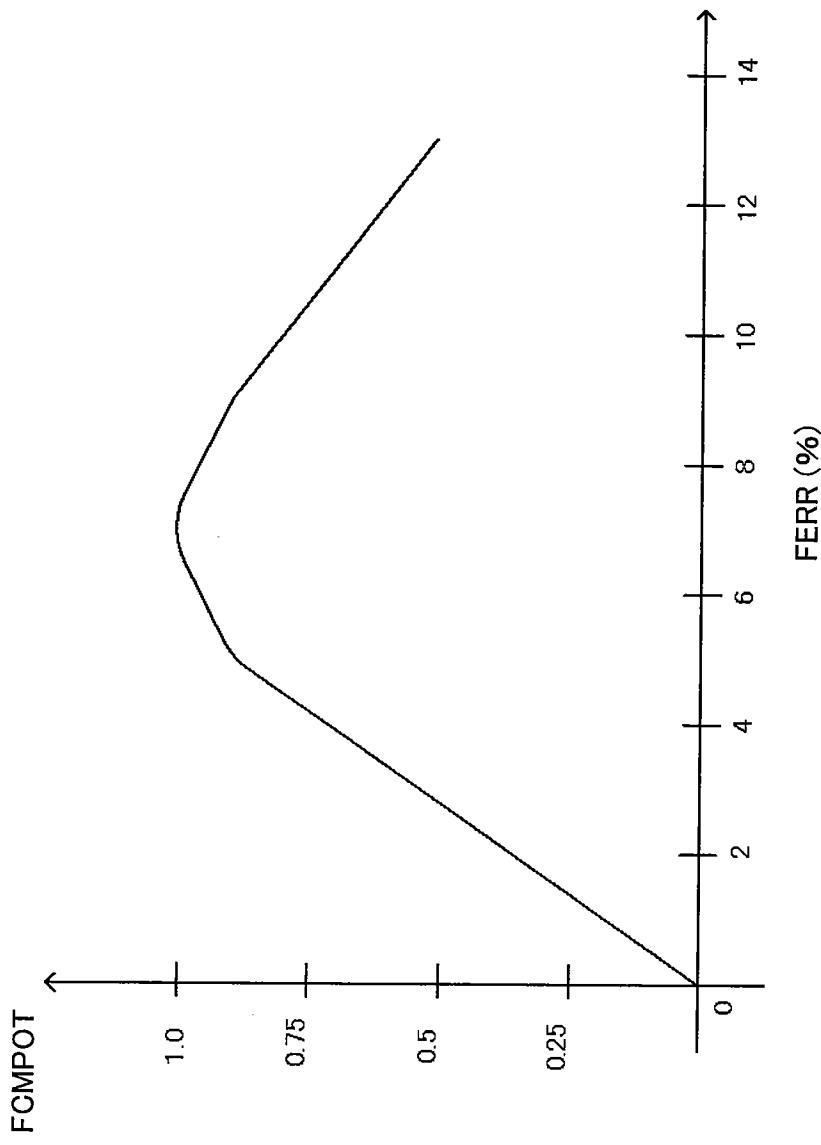


FIG. 8  
CASE OF BD ROM (THEORETICAL VALUE)



(CASE WHERE INTEGRATION SETTING VALUE = 8, JUDGEMENT THRESHOLD VALUE = 4, UPM IS OUTPUT)

**FIG. 9A** CLK



**FIG. 9B** UP



**FIG. 9C** DOWN



**FIG. 9D** UP COUNTER



**FIG. 9E** DOWN COUNTER



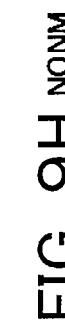
**FIG. 9F** UPM



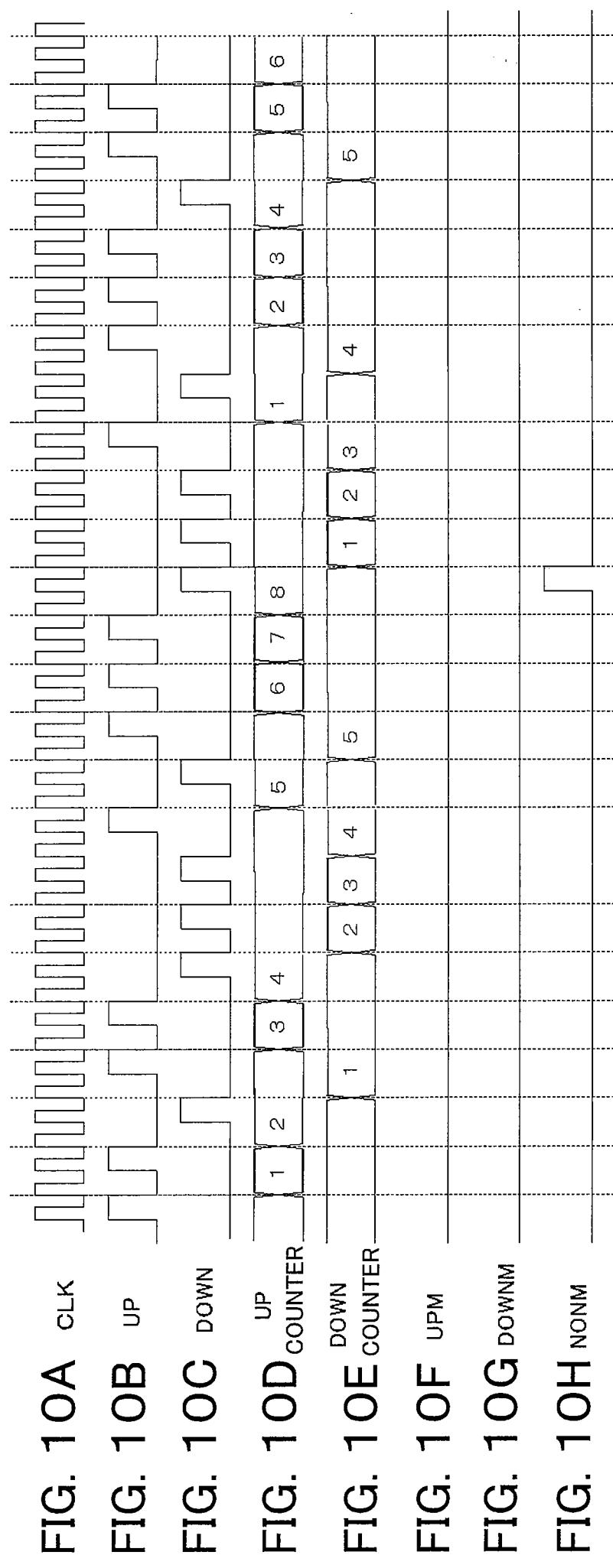
**FIG. 9G** DOWNM



**FIG. 9H** NONM



(CASE WHERE INTEGRATION SETTING VALUE = 8, JUDGEMENT THRESHOLD VALUE = 4, NONM IS OUTPUT)



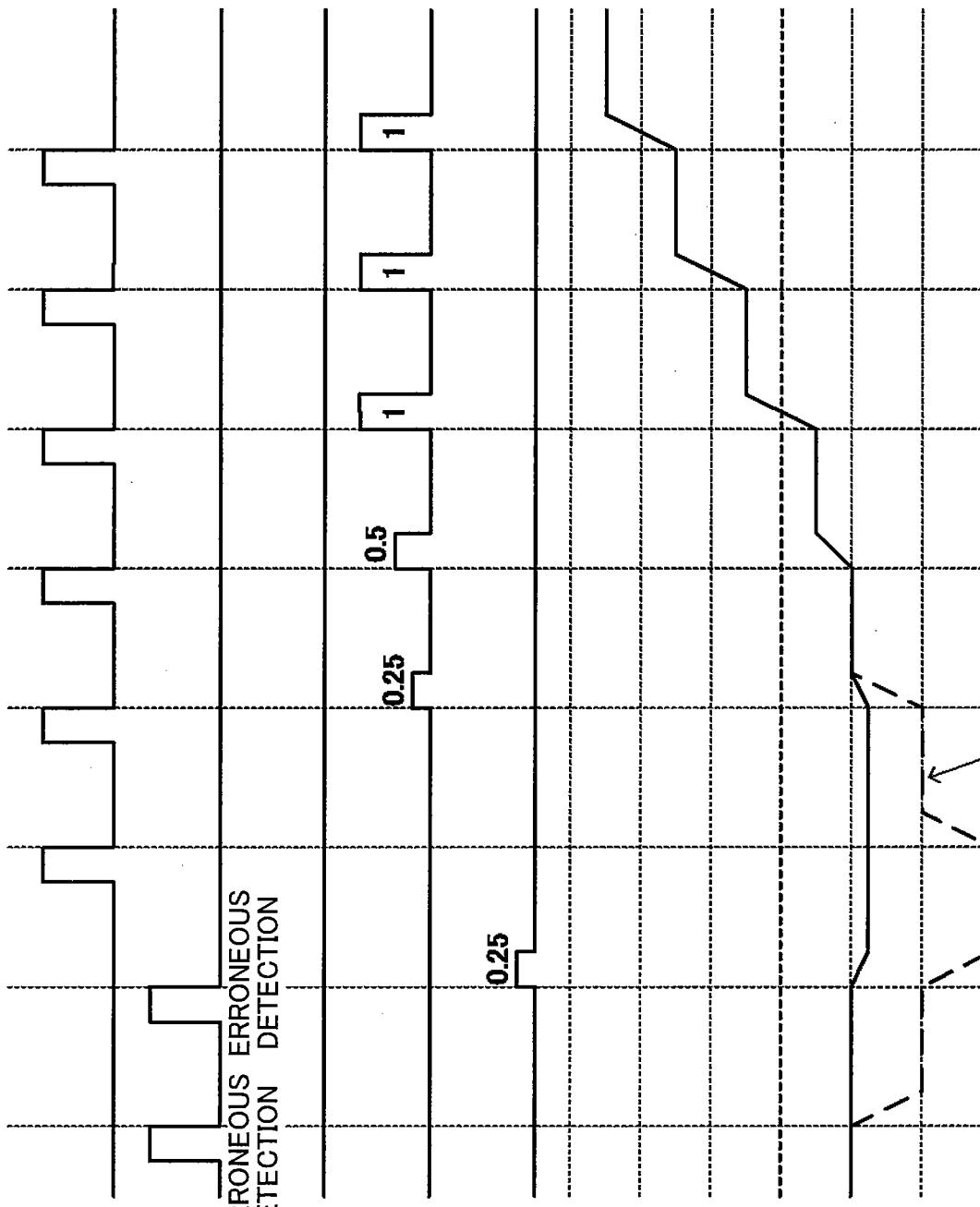
**FIG. 11**

THREE TIME PREVIOUS COMPARATOR OUTPUT	TWO TIME PREVIOUS COMPARATOR OUTPUT	ONE TIME PREVIOUS COMPARATOR OUTPUT	PRESENT COMPARATOR OUTPUT	OUTPUT	
				UPM	GAIN (PULSE WIDTH)
OTHER THAN UPM	EITHER IS UPM	OTHER THAN UPM	NONE	0	
OTHER THAN UPM	OTHER THAN UPM	UPM	UPOUT	0.25(1T)	
OTHER THAN UPM	UPM	UPM	UPOUT	0.25(1T)	
UPM	UPM	UPM	UPOUT	0.5(2T)	
OTHER THAN DOWNM	EITHER IS DOWNM	OTHER THAN DOWNM	NONE	0	
OTHER THAN DOWNM	DOWNM	DOWNM	DOWNOUT	0.25(1T)	
OTHER THAN DOWNM	DOWNM	DOWNM	DOWNOUT	0.25(1T)	
DOWNM	DOWNM	DOWNM	DOWNOUT	0.5(2T)	
ALL	ALL	NONM	NONM	1(4T)	0

### INITIAL OPERATION OF FREQUENCY LOCK-IN

**FIG. 12A**

UPM



**FIG. 12B**

DOWNM

ERRONEOUS  
DETECTION

NONM

**FIG. 12C**

UPOUT  
GAIN

DOWNTOUT  
GAIN



**FIG. 12E**

VCO  
CONTROL  
VOLTAGE

**FIG. 12F**

PATTERN JUDGMENT  
& GAIN ADJUSTMENT  
ARE NOT CARRIED OUT

PATTERN JUDGMENT  
& GAIN ADJUSTMENT  
ARE NOT CARRIED OUT

PATTERN JUDGMENT  
& GAIN ADJUSTMENT  
ARE NOT CARRIED OUT

## OPERATION DURING FREQUENCY LOCK-IN

FIG. 13A UPM

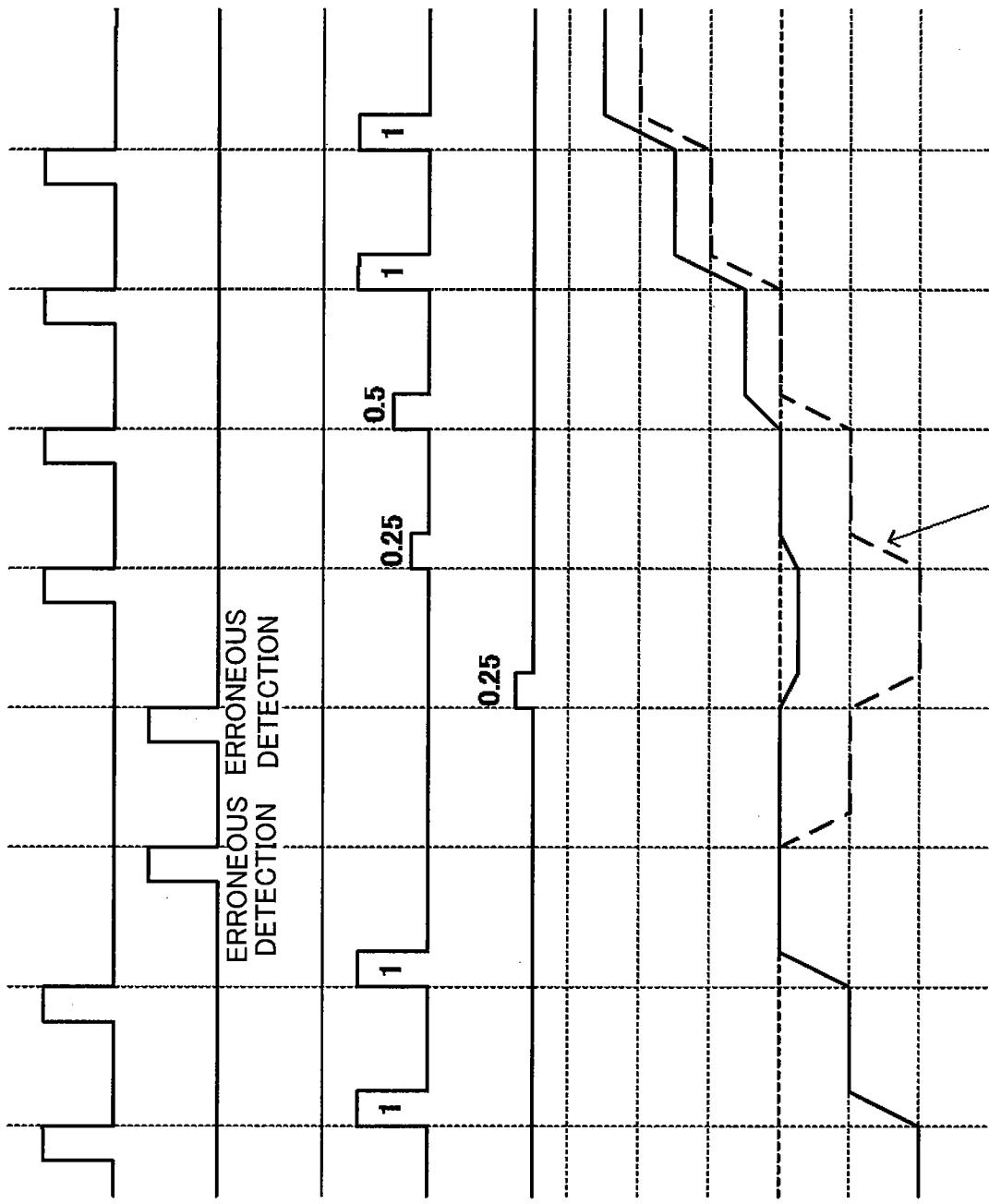


FIG. 13B DOWNM

FIG. 13C NONM

FIG. 13D UPOUT GAIN

FIG. 13E DOWNOUT GAIN

FIG. 13F VCO CONTROL VOLTAGE

PATTERN JUDGMENT  
& GAIN ADJUSTMENT  
ARE NOT CARRIED OUT

## OPERATION AT CONVERGENCE

FIG. 14A UPM

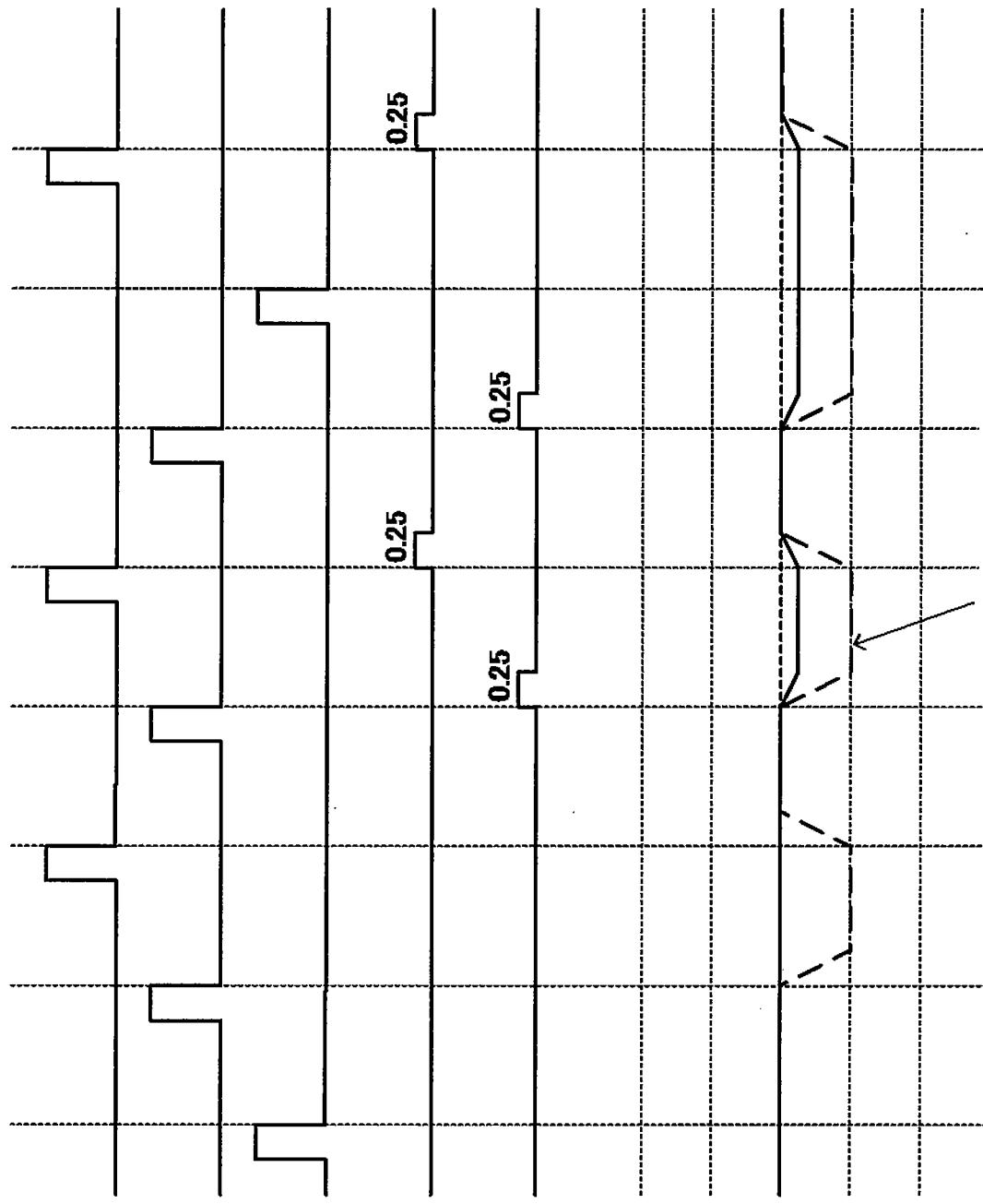


FIG. 14B DOWNM

FIG. 14C NONM

FIG. 14D UPOUT  
GAINFIG. 14E DOWNTOUT  
GAINFIG. 14F VCO  
CONTROL  
VOLTAGE

PATTERN JUDGMENT  
& GAIN ADJUSTMENT  
ARE NOT CARRIED OUT